





1ST INTERNATIONAL CONFERENCE

on

ARTIFICIAL INTELLIGENCE & MACHINE LEARNING IN COMMUNICATION AND POWER SYSTEMS (AIMLCPS-2026)



20th– 21st February, 2026
Organized By:
 Department of Electrical Engineering
 &
 Department of Electronics & Telecommunication Engineering
 Parala Maharaja Engineering College
(A Govt. Engg. College Affiliated to BPUT Rourkela)
 Berhampur, Odisha, India

Technically Co-Sponsored By
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About the Conference

1st International Conference on “Artificial Intelligence & Machine Learning in Communication and Power Systems” AIMLCPS-2026 (IEEE Conference record number 68702) is being organized by the Department of Electrical Engineering and Department of Electronics & Telecommunication, Parala Maharaja Engineering College, Berhampur, Odisha, India, during 20th – 21st February 2026 in **hybrid mode**. The objective is to provide a comprehensive platform for academicians, scientists, researchers, engineers, and students to stay updated on recent trends and likely future research directions in Electronics and Electrical Engineering at the intersection of AI/ML. The conference aims to foster the theme through keynote addresses, invited talks, and oral presentations of research articles in the most relevant areas related to the conference theme. The conference will also provide an international platform for delegates to share research ideas, exchange scientific thoughts and open possibilities of new collaborations on research problems. Papers submitted to AIMLCPS-2026 should contain original research/study and should not have been published or submitted for publication elsewhere. Accepted and presented papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE scope and quality requirements. The IEEE antiplagiarism policy is applicable to all submissions. At least one author of each accepted paper must register for the conference and present his/her paper.

Conference Webpage: <https://pmec.ac.in/aimlcps-2026/>

Call for Papers

Prospective authors are invited to submit research papers in IEEE format describing original research contributions in the following (but not limited to) areas:

- Track 1: AI & ML in Smart Grids and RES Integration
- Track 2: Intelligent Control & Protection of Power System
- Track 3: AI in Power Electronics, EV, and Energy Storage
- Track 4: AI & ML for Next-Generation Comm. Networks
- Track 5: Intelligent Signal and Image Processing
- Track 6: VLSI and Hardware Architectures for AI & ML
- Track 7: AI & ML in Electronic Devices Modeling
- Track 8: Intelligent Antenna Design

Important Dates/Deadlines

Submission of Full Papers	November 21, 2025
Notification of Acceptance	January 21, 2026
Camera Ready Submission	January 31, 2026
Early Bird Registration	January 31, 2026
Registration (with late fee of INR 500)	February 10 2026
Conference Date	February 20-21 2026

Submission Instruction

- Papers must be submitted online through the **Microsoft CMT** platform using **this submission link**
- For detailed submission guidelines and paper templates please visit “Call for papers” in the conference **webpage**



Other Highlights

- Eight keynote talks by international/ National Experts
- Oral and Poster presentations
- Industry exhibition
- Peer networking
- Presented papers will be Published in IEEE Xplore

Early Bird Registration Fee

Category	Indian Author	Foreign Author
Academician/Scientists (Non-IEEE Member)	INR 6000 /-	USD 100
Researcher/Academician/Scientists (IEEE Member)	INR 5500 /-	USD 80
Student / Research Scholar (Non-IEEE Members)	INR 5000/-	USD 70
Student / Research Scholar (IEEE Student Members)	INR 4500/-	USD 60
Industry Professionals	INR 7000/-	USD 200

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